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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Applicants:	Nayfeh et al.	I hereby certify that this paper is being deposited with
Serial No.:	09/496,506) the United States Postal Service as FIRST-CLASS may in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on this date.
Filed:	February 2, 2000) 3/9/00 Date Registration No. 33/32
For:	SILICON NANOPARTICLE FIELD EFFECT TRANSISTOR AND TRANSISTOR MEMORY DEVICE	F-CLASS.WCM) Appr. February 20, 1998 Attorney for Applicant)
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PATENT

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents Washington, D.C. 20231

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Dear Sir:

Art Unit:

In accordance with 37 C.F.R. §§1.56, 1.97 and 1.98, applicants through counsel

herewith:

Submit copies of the patents set forth in the attached form PTO-1449 as

follows:

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Applicants respectfully request that the Examiner consider the above-listed references in the examination of this application and list these references of record in the application.

Respectfully submitted,

GREER, BURNS & CRAIN, LTD.

By

Steven P. Fallon

Registration No. 35,132

May 9, 2000

Suite 8660 - Sears Tower 233 South Wacker Drive Chicago, Illinois 60606 (312) 993-0080 F\DATA\WP60\1201\63407\IDS.FRM